Requirements for FlockLab Target Adapters

**Header Connector**
on target adapter board:
- 2x12 pin male header
- pitch spacing 2.54mm
- pin height max. 6mm
example: part no. 67997-224HLF

**Header Signal Description**
- VCC_3.3: 3.3V input voltage
- VCC_5.0: 5V input voltage
- VCC_TARGET: adjustable target voltage
- UART_RXD: UART receive (target output)
- UART_TXD: UART transmit (target input)
- PROG: bootstrap entry pin (target input for reprogramming via BSL)
- nRST: reset (active low, target input)
- SIG1: actuation pin (target input)
- SIG2: actuation pin (target input)
- INT1: tracing pin (target output)
- INT2: tracing pin (target output)
- LED1: tracing pin (target output)
- LED2: tracing pin (target output)
- LED3: tracing pin (target output)
- SERIALID: serial ID chip line
- USB_P: USB positive
- USB_N: USB negative
- SWDIO: serial wire debug I/O
- SWDCLK: serial wire debug clock
- SWD: serial wire output (target output)
- N/C: not connected (unused)
- GND_SENSED: ground (sensed)
- GND: ground (not sensed)

**Voltage levels:**
All signal lines have the same voltage as VCC_TARGET except for USB_P, USB_N and SERIALID (always 3.3V).

---

**ID Chip (DS 2401P)**
Pull-up for SERIALID is already placed on the observer.